**American International University- Bangladesh (AIUB)**

**Faculty of Engineering (FE)**

**Department of Electrical and Electronic Engineering (EEE)**



**Course Project Report Outline (Electronic Devices Lab)**

1. **Download the attached IEEE template for report writing from the link given in TEAMS.**
2. **Title, Abstract (at least 150 words but not more than 300 words) and Keywords (3-6 keywords separated by a comma).** [2 marks]
3. **Introduction**

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| --- |
| * 1. Background of Study and Motivation [1 mark] |
| * 1. Project Objectives [1 mark] |
| * 1. A brief Outline of the Report [1 mark] |

1. **Literature Review *(At least 5 project-related published journal papers within the year 2018 to 2022)***

[3 marks]

1. **Methodology and Modeling**

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| --- |
| * 1. Introduction [1 mark] |
| * 1. Working Principle of the Proposed Project [1 mark] |
| * + 1. Process of Work [1 mark] |
| * 1. Description of the Components [1 mark] |
| * 1. Test/Experimental Setup [1 mark] |

1. **Results and Discussions**

|  |
| --- |
| * 1. Simulation/Numerical Analysis [1 mark] |
| * 1. Measured response/Experimental Results [1 mark] |
| * 1. Comparison between Numerical and Experimental Results [1 mark] |
| * 1. Cost Analysis [1 mark] |
| * 1. Limitations in the Project [1 mark] |

1. **Conclusion and Future Endeavors** [1 mark]

**References** [1 mark]

**Appendix (if any, optional)**

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| AIUB | | **American International University-Bangladesh (AIUB)**  **Faculty of Engineering (FE)**  **Department of Electrical and Electronic Engineering (EEE)** | | | |
|  | | |  |  |  | |
| **Course Name:** | | |  | **Course Code:** | EEE | |
| **Semester:** | | | Spring 2023-2024 | **Section:** |  | |
| **Faculty Name:** | | | **Md. Shaoran Sayem** | | | |
|  | | |  |  |  | |
| **Capstone Project Title:** | | |  | | | |
| **Project Group #:** | | |  | | | |
|  | | |  |  |  | |
| **SL #** | **Student Name** | | | **Student ID #** | | |
| **1.** |  | | |  | | |
| **2.** |  | | |  | | |
| **3.** |  | | |  | | |
| **4.** |  | | |  | | |
| **5.** |  | | |  | | |
| **6.** |  | | |  | | |

***Assessment Materials and Marks Allocation:***

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **CO/**  **CLO Number** | **CO/CLO Statement** | **K** | **P** | **A** | **Assessed Program Outcome Indicator** | **BNQF Indicator** | **Teaching-Learning Strategy** | **Assessment Strategy** |
| 2 | Select engineering tools (e.g., MULTISIM) to simulate different electronic circuit problems considering the limitations. | K6 | P1  P4 P5 |  | **P.e.1.C6** | FS.6 |  | Capstone Project Report |
| 3 | Communicate effectively by giving and responding to clear instructions to produce an effective presentations for complex engineering solutions |  |  | A1  A3 A5 | **P.j.1. A2** | SS.1 |  | Capstone Project Presentation |

***Assessment Rubrics:***

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| **COs** | **Excellent to Proficient [5-4]** | **Good [3]** | **Acceptable [2]** | **Unacceptable [1]** | **No Response [0]** | **Secured Marks** |
| **CO3**  **P.d.1.P3** | The outcome of the project demonstrates a course project using semiconductor devices, passive components, etc. that can solve a complex engineering problem in the electrical and electronic engineering discipline through appropriate research. | The outcome of the project somewhat demonstrates a course project using semiconductor devices, passive components, etc., and also somewhat solves a complex engineering problem in the electrical and electronic engineering discipline through some research. | The outcome of the project demonstrates a course project using semiconductor devices, passive components, etc. but cannot solve a complex engineering problem properly in the electrical and electronic engineering discipline through appropriate research. | The outcome of the project does not demonstrate a course project using semiconductor devices, passive components, etc. also could not solve a complex engineering problem in the electrical and electronic engineering discipline through appropriate research. | No Response at all/copied from others/ identical submissions with gross errors/image file printed |  |
| **Comments** |  | | | | **Total Marks (5)** |  |